



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

SHIMODA, et al.

Serial No.:

10/521,470

Filed:

January 18, 2005

Title:

Multilayer Wiring Board, Manufacturing Method Thereof,

Semiconductor Device, and Wireless Electronic Device

Group:

2812

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

January 26, 2007

Sir:

Enclosed is a copy of the Filing Receipt for the above-identified application which incorrectly states the tenth inventor's first name as Kazumori. It should read Kazunori.

The correction is shown on the attached copy of the filing receipt. Issuance of a corrected filing receipt is respectfully requested. The changes are supported by the Declaration filed on January 8, 2005, copy attached herewith.

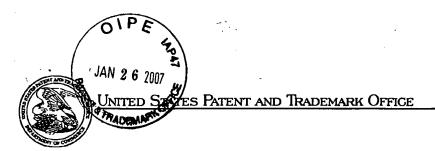
Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

William I. Solomon

Registration No. 28,565

WIS/stv Attachments



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FILING OR 371 ART UNIT APPL NO. FIL FEE REC'D ATTY.DOCKET NO DRAWINGS TOT CLMS IND CLMS (c) DATE **40/521,470 \01/18/2005** 2812 13250 \1204.44657X00 \7 \ 59 ₹5

20457

CONFIRMATION NO. 2249

FILING RECEIPT

OC000000017562916

1919

ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 NORTH SEVENTEENTH STREET

SUITE 1800 ARLINGTON, VA 22209-3873

Date Mailed: 12/05/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Yasushi Shimada, Tsukuba-shi, Ibaraki, JAPAN: Yoshitaka Hirata, Shimodate-shi, Ibaraki, JAPAN: \Hiroyuki Kuriya, Shimodate-shi, Ibaraki, JAPAN;

\Kazuhisa Otsuka, Shimodate-shi, Ibaraki, JAPAN;

Masanori Yamaguchi, Tsukuba-shi, Ibaraki, JAPAN:

Yuichi Shimayama, Shimodate-shi, Ibaraki, JAPAN;

\ Ken Madarame, Yuki-shi, Ibaraki, JAPAN;

\ Etsuo Mizushima, Yuki-shi, Ibaraki, JAPAN;

Yuusuke Kondou, Shimodate-shi, Ibaraki, JAPAN;

TKazumori)Yamamoto, Tsukuba-shi, Ibaraki, JAPAN; ensterd of Kazuman

Assignment For Published Patent Application

Hitachi Chemical Co., Ltd., Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 20457.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/06860 05/30/2003

Foreign Applications

VAPAN 2002-209650 07/18/2002 VAPAN 2002-209639 07/18/2002 VAPAN 2002-259291 09/04/2002 VAPAN 2002-259284 09/04/2002 \JAPAN 2002-324238 11/07/2002

Projected Publication Date: 03/09/2006

Non-Publication Request: No

Early Publication Request: No

Title

Multilayer wiring board, method for producing the same, semiconductor device and radio electronic device

Preliminary Class

438

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

Applicants also are advised that in the case of inventions made in the United States, the Director of the USPTO must issue a license before applicants can apply for a patent in a foreign country. The filing of a U.S. patent application serves as a request for a foreign filing license. The application's filing receipt contains further information and guidance as to the status of applicant's license for foreign filing.

Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at http://www.uspto.gov/web/offices/pac/doc/general/index.html.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, http://www.stopfakes.gov. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4158).

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15

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Attorney's Docket No.:

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that: my residence, post office address and country of citizenship are as stated below, next to my name; I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

or PCT International Application Number __PCT/JP2003/006860

I hereby state that I have reviewed and understand the contents of the above-identified specification,

including the claim(s), as amended by any amendment referred to above. I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations,

(if applicable)

MULTILAYER WIRING BOARD, MANUFACTURING METHOD THEREOF, SEMICONDUCTOR DEVICE, AND WIRELESS ELECTRONIC DEVICE the specification of which is attached hereto.

and was amended on November 3, 2003

30/05/2003

United States Application Number

was filed on

Section 1.56.

is claimed:	or s cordinate having a fini	ng date before that of the applica	Prior	
Prior Foreign Application(s)			Clair	•
2002-209650	Japan	18/07/2002	х	
(Number)	(Country)	(Day/Month/Year Filed)	<u>X</u> Yes	No
2002-209639	Japan	18/07/2002	<u>X</u> _	
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
2002-259291	Japan	04/09/2002	x	
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
2002-259284	Japan	04/09/2002	_X_	
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
2002-324238	<u>Japan</u>	07/11/2002	_X_	<u>.</u>
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No
I hereby claim the benefit und application(s) listed below	er title 35, United States C	Code, Section 119(e) of any Un	ited States	provisiona
(Application Number)	Filing Date			
(Application Number)	Filing Date			

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States

application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose all information known to me to be material to

patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Number)

Filing Date

(Status -- patented,

pending, abandoned)

(Application Number)

Filing Date

(Status -- patented,

pending, abandoned)

I hereby appoint: Donald R. Antonelli, Reg. No. 20,296; Melvin Kraus, Reg. No. 22,466; William I. Solomon, Reg. No. 28,565; Gregory E. Montone, Reg. No. 28,141; Ronald J. Shore, Reg. No. 28,577; Donald E. Stout, Reg. No. 26,422; Alan E. Schiavelli, Reg. No. 32,087; James N. Dresser, Reg. No. 22,973; Carl I. Brundidge, Reg. No. 29,621; Paul J. Skwierawski, Reg. No. 32,173; and Robert M. Bauer, Reg. No. 34,487, my attorneys; of ANTONELLI, TERRY, STOUT & KRAUS, LLP with offices located at 1300 North Seventeenth Street, Suite 1800, Arlington, Virginia 22209, telephone: (703) 312-6600, fax: (703) 312-6666; with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

Send all correspondence to:

Customer Number 020457
ANTONELLI, TERRY, STOUT & KRAUS, LLP
1300 North Seventeenth Street
Suite 1800
Arlington, VA. 22209

Direct all telephone calls and faxes to:

TEL: (703) 312-6600 FAX: (703) 312-6666

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full Name of Sole/Fire				
Inventor's Signature _	Gambi s	Shinada	D	ate January 11, 2005
Residence Tsukuba-			Citizenship	
Post Office Address	(City, Sta c/o HITACH		TD., Research a	(Country) and Development Center,
	1500, Oaza	Ogawa, Shimodate-	shi, Ibaraki 308	-8521 Japan
Full Name of Second/	Joint Inventor _	Yoshitaka HIRAT.	A .	·
Inventor's Signature _	yorker	L bolata	D	ate January 11, 2005
Residence Shimodat			Citizenship	Japanese
Post Office Address	(City, Sta c/o HITACH		.TD., Research a	(Country) and Development Center,
	1500, Oa	aza Ogawa, Shimoda	te-shi, Ibaraki 3	308-8521 Japan

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*	
	Full Name of Third/Joint Inventor Hiroyuki KURIYA
	Inventor's Signature Kiroyuhi Muriya Date January 11, 2005
	· · · · · · · · · · · · · · · · · · ·
	Residence Shimodate-shi, Ibaraki, Japan Citizenship Japanese (City, State) (Country)
	Post Office Address c/o HITACHI CHEMICAL CO., LTD., 1-1, Nishishinjuku 2-chome, Shinjuku-ku
	Tokyo 163-0449 Japan
	Full Name of Fourth/Joint Inventor Kazuhisa OTSUKA
	Inventor's Signature <u>Hazulisa Otsuka</u> Date <u>January 11, 2005</u>
	Residence Shimodate-shi, Ibaraki, Japan Citizenship Japanese (City, State) (Country)
	Post Office Address c/o HITACHI CHEMICAL CO., LTD., Research and Development Center,
	1500, Oaza Ogawa, Shimodate-shi, Ibaraki 308-8521 Japan
	Full Name of Fifth/Joint Inventor Masanori YAMAGUCHI
	Inventor's Signature Mayny Tamagadu Date January 11, 2005
	Residence Tsukuba-shi, Ibaraki, Japan Citizenship Japanese
	(City, State) (Country) Post Office Address c/o HITACHI CHEMICAL CO., LTD., Research and Development Center,
	1500, Oaza Ogawa, Shimodate-shi, Ibaraki 308-8521 Japan
•	Full Name of Sixth/Joint Inventor Yuichi SHIMAYAMA
	Muichi Thimayama
	micholor o digitating grant gr
	Residence Shimodate shi, Ibaraki, Japan Citizenship Japanese (Country)
	Post Office Address <u>c/o HITACHI CHEMICAL CO., LTD., Research and Development Center,</u>
	1500, Oaza Ogawa, Shimodate-shi, Ibaraki 308-8521 Japan
	Full Name of Seventh/Joint Inventor Ken MADARAME
	Inventor's Signature Ken Madarame Date January 11, 2005
	Residence Yuki-shi, Ibaraki, Japan Citizenship Japanese (City, State) (Country)
*	Post Office Address
	1500, Oaza Ogawa, Shimodate-shi, Ibaraki 308-8521 Japan

	•	•
Full Name of Fi	ighth/Joint Inventor Etsuo MIZUSHIMA	
Inventor's Signa	C MI /	nuary 11, 2005
_		panese
Post Office Add	(City, State)	(Country)
	1500, Oaza Ogawa, Shimodate-shi, Ibaraki 308-8521 Jap	oan
Full Name of Ni	inth/Joint Inventor Yuusuke KONDOU	
Inventor's Signa	ature Yuusuke Kondou Date J	anuary 11, 2005
Residence Shi	modate-shi, Ibaraki, Japan Citizenship Japan (City, State)	canese (Country)
Post Office Add		
•	1500, Oaza Ogawa, Shimodate-shi, Ibaraki 308-8521 Jan	oan
Full Name of Te	enth/Joint Inventor Kazunori YAMAMOTO -	
Inventor's Signa	ature Knz nnoH Jnmamol Date J	anuary 11, 2005
Residence Tsul	kuba-shi, Ibaraki, Japan Citizenship Ja	
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